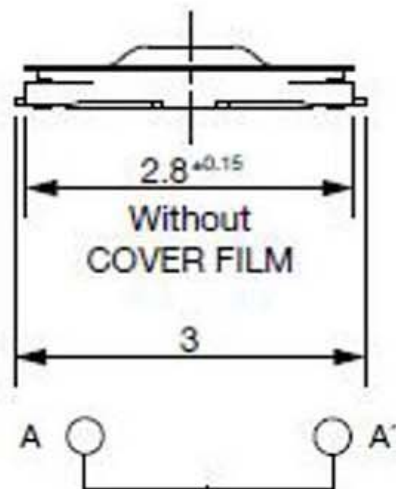
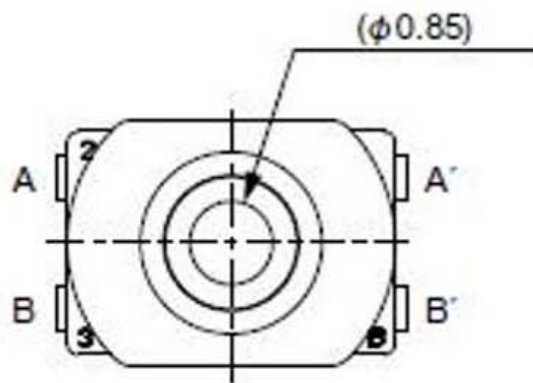
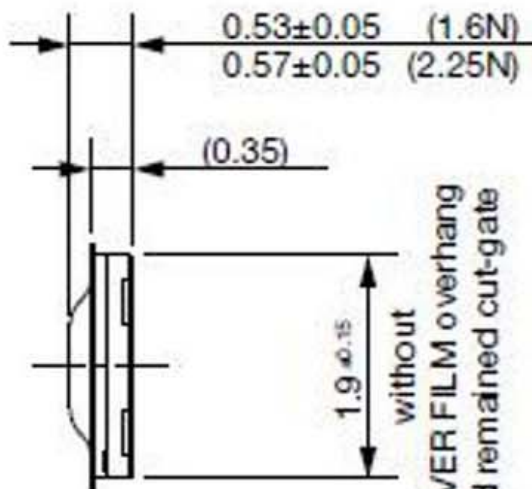


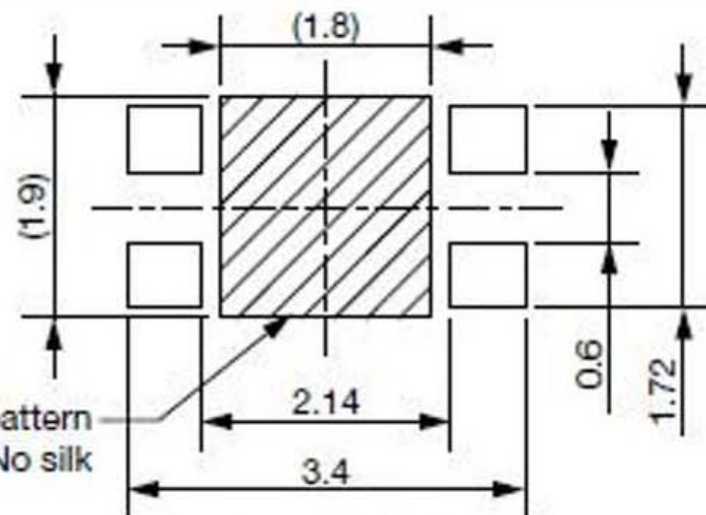


Circuit diagram



without
COVER FILM overhang
and remained cut-gate

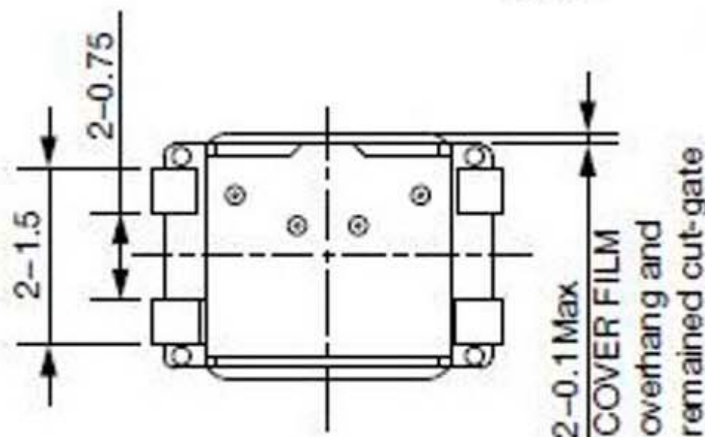
No pattern
No silk



Reference land dimension
The thickness of solder stencil : 80µm

SPECIFICATIONS

Rating	DC 12V 50mA.		
Insulation Resistance	50 MΩ Min.		
Contact Resistance	100 mΩ Max.		
Operating Force	2	160 ± 50gf	3 260 ± 50gf
Operating Life (cycles)	50,000		



2-0.1 Max
COVER FILM
overhang and
remained cut-gate

E	TERMINAL	3	PHOSPHOR BRONZE	Ag plated
D	CONTACT	2	SUS	Ag plated
C	COVER	1	PHOSPHOR BRONZE	Ag plated
B	BASE	1	PA6T	Black
A	STEM	1	PPA	White
NO.	DESCRIPTION	QTY	MATERIAL	PLATING

7	新增尺寸	Jack	010918
6	更新画面尺寸	Jack	112317
5	更新PCB LAYOUT模式及尺寸	Jack	020917
4	更新PCB LAYOUT模式	Jack	011717
3	更新SPEC及材質	Jack	061515
2	更新尺寸標示	Jack	050615
1	新增PIN編號	Jack	020915
ITEM NO.	DESCRIPTION	DRAWN	DATE



东莞市高迪电子有限公司

Dong Guan KDRR Electronic Switch Jack Co., Ltd.

TOLERANCE UNLESS
OTHERWISE STATED :

Up to 5 ±0.2
Above 5 ~ 15 ±0.3
Above 15 ~ 30 ±0.4
Above 30 ~ 50 ±0.5
Angle ±0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY:

Jack Lu

DATE

01/03/18

CHECKED BY:

Jacky Chen

DATE

01/03/18

APPROVED BY:

Tony Kao

DATE

01/03/18

MAT'L

FINISH

SCALE

1 : 1

TITLE

SWITCH

MODLE

TACT SWITCH

DWG NO.

PART NO.

SIZE

A4

VER

R7