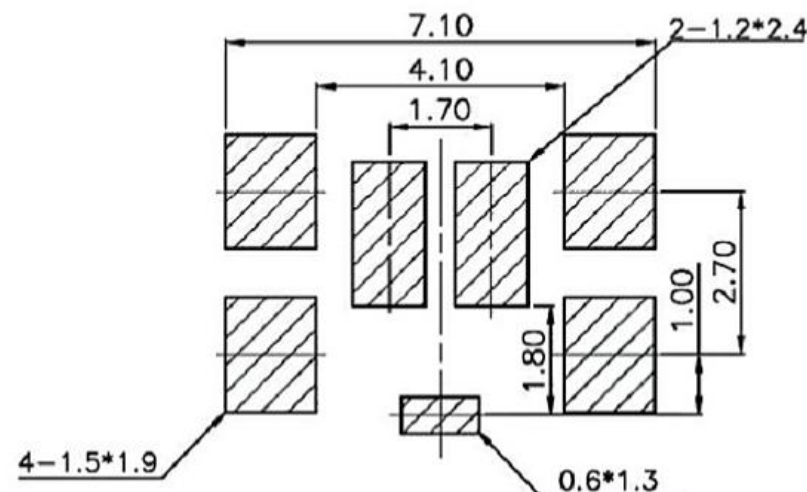
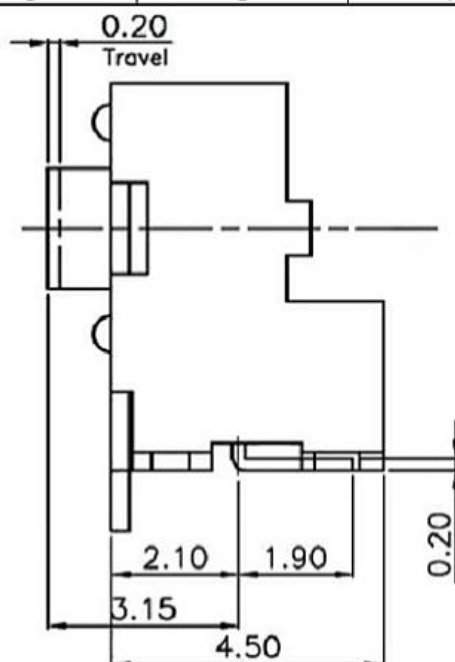
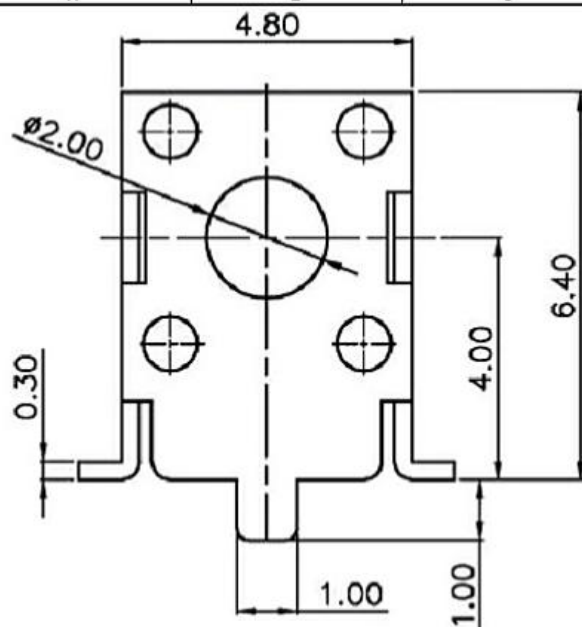
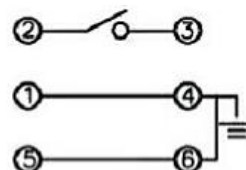




PCB Layout(Pattern side)



Circuit Diagram

SPECIFICATIONS

Rating	DC 12V 50mA.		
Insulation Resistance	50 M Ω Min.		
Contact Resistance	100 m Ω Max.		
Operating Force	2	160 $\pm$ 50gf	3 260 $\pm$ 50gf
Operating Life (cycles)	50,000		

Operating force
180gf $\pm$ 30gf
250gf $\pm$ 30gf

E	TERMINAL	3	PHOSPHOR BRONZE	Ag plated
D	CONTACT	2	SUS	Ag plated
C	COVER	1	PHOSPHOR BRONZE	Ag plated
B	BASE	1	PA6T	Black
A	STEM	1	PPA	White
NO.	DESCRIPTION	QTY	MATERIAL	PLATING

7	新增尺寸	Jack	010318
6	更新侧面尺寸	Jack	112317
5	更新PCB LAYOUT模式及尺寸	Jack	020317
4	更新PCB LAYOUT模式	Jack	011717
3	更新SPEC及材质	Jack	061515
2	更新尺寸标示	Jack	050615
1	新增PIN脚	Jack	020315
ITEM NO.	DESCRIPTION	DRAWN	DATE



东莞市高迪电子有限公司

Dong Guan KDRR Electronic Switch Jack Co., Ltd.

TOLERANCE UNLESS
OTHERWISE STATED:

Up to 5 $\pm$ 0.2
Above 5 ~ 15 $\pm$ 0.3
Above 15 ~ 30 $\pm$ 0.4
Above 30 ~ 50 $\pm$ 0.5
Angle $\pm$ 0.3°

3RD.
ANGEL'S



UNITS

MM

DRAWN BY:	DATE	MAT'L	TITLE	SWITCH
Jack Lu	01/03/18			
CHECKED BY:	DATE	FINISH	MODLE	TACT SWITCH
Jacky Chen	01/03/18			
APPROVED BY:	DATE	SCALE	DWG NO.	
Tony Kao	01/03/18	1 : 1		
		SHEET NO.	PART NO.	
		1 of 1		

SIZE
A4
VER
R7