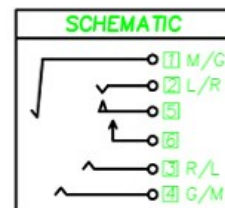
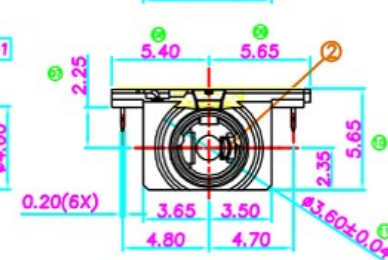
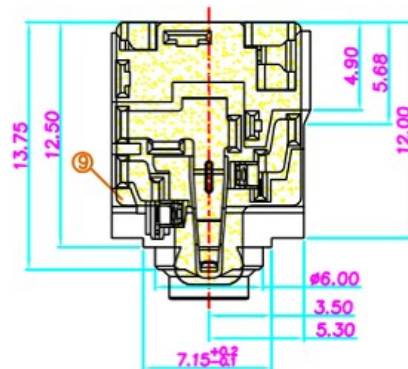
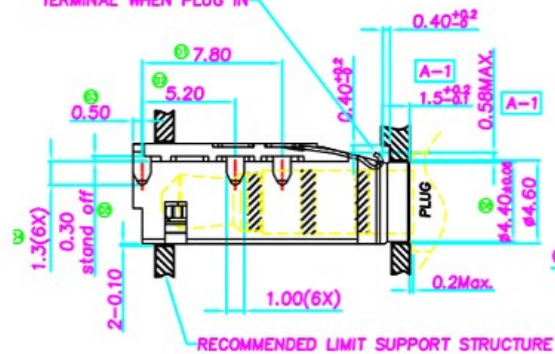
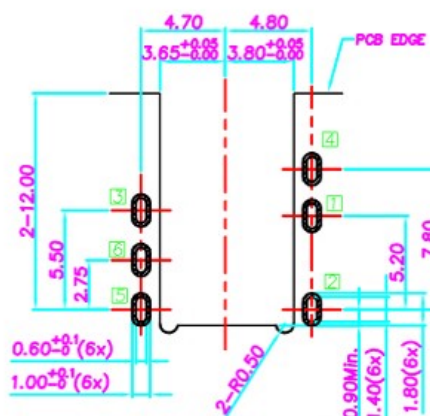
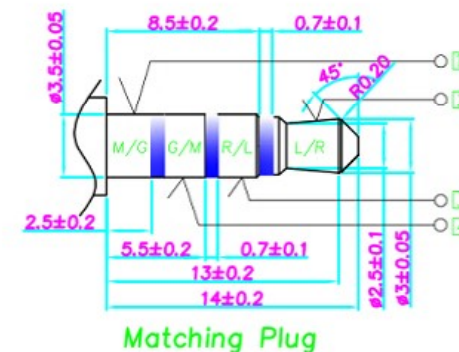
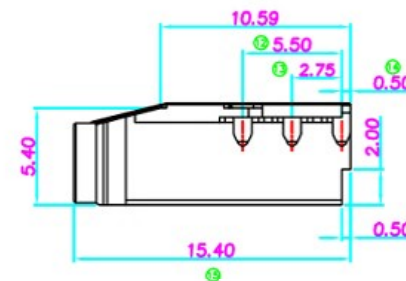
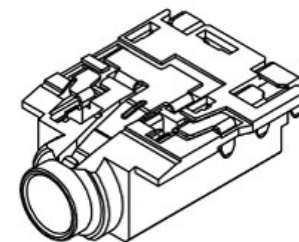


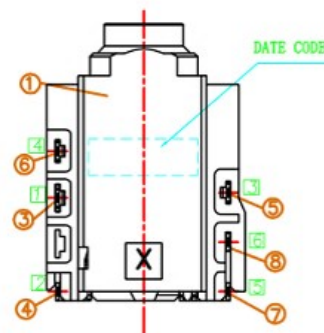
MATCH MECHANISM ID
NEEDS TO AVOID
INTERFERENCE WITH
TERMINAL WHEN PLUG IN



□: Terminal No.
①: Project No.



**RECOMMENDED PCB LAYOUT
TOP VIEW (TOLERANCE: ±0.05)**



⑨	Insulator	1	PA10T UL94 V-0	Black
⑧	Housing	1	PA10T UL94 V-0	Black
⑦	Shell		C5210, 0.20t	Nickel 50u" Plating
⑥	Shunt terminal	⑥	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
⑤	Transfer Spring	⑤	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
④	Ring spring	④	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
③	Ring spring	③	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
②	Tip spring	②	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
①	Earth spring	①	1 C5210, 0.20t	Gold Flash On Contact Area and Solder Tail Area, All over 50u" Nickel Plated
NO.	DESCRIPTION	QTY	MATERIAL	REMARK

					Angle	±2°	DESIGN	罗振强	2021.05.18
					. x	±0.35	CHECK		
					. xx	±0.25	APPROVAL		
					. xxx	±0.10			
ECN NO.	REV	DATE	DESCRIPTION	CHANGE	APPRO.	GENERAL TOLERANCE UNLESS OTHERWISE NOTED	SCALE: 1:1	UNIT:mm	A4

东莞市高迪电子有限公司

CUSTOMER DRAWING	TITLE:	φ3.5mm PHONE JACK
	MODEL NO.	
	FILE NO.	Q/DY. QW-001A-0745